



Attorney Docket No. 0756-7225

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shunpei YAMAZAKI et al.
Serial No. 10/728,933
Filed: December 8, 2003
For: METHOD FOR MANUFACTURING
SEMICONDUCTOR APPARATUS,
AND SEMICONDUCTOR
APPARATUS AND ELECTRIC
APPLIANCE

-) Group Art Unit: 2891
) Examiner: D. Zarneke
)
) CERTIFICATE OF MAILING
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) 2005
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)
)

RESPONSE

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The Official Action mailed September 28, 2005, has been received and its contents carefully noted. This response is filed within three months of the mailing date of the Official Action and therefore is believed to be timely without extension of time. Accordingly, the Applicants respectfully submit that this response is being timely filed.

The Applicants note with appreciation the consideration of the Information Disclosure Statements filed on December 8, 2003; and November 4, 2004. A further Information Disclosure Statement is submitted herewith and consideration of this Information Disclosure Statement is respectfully requested.

Claims 1-17 are pending in the present application, of which claims 1-7 are independent. Claims 3, 4, 6 and 7 have been withdrawn from consideration by the Examiner (Office Action Summary, Paper No. 0509). Accordingly, claims 1, 2, 5 and 8-17 are currently elected, of which claims 1, 2 and 5 are independent, and claims 1 and 8-17 as they depend from claim 1 are generic. For the reasons set forth in detail below,